

**AMENDMENTS TO THE CLAIMS**

1. (Currently Amended) A liquid crystal display device comprising:
a plurality of gate lines on a layer on a surface of a substrate;
a plurality of data lines, crossing said gate lines, such that at least one pixel region is defined by the data and gate lines, at least one of said data lines defining the pixel region having a first data line section and a second data line section, the first data line section and the second data line section intersecting at a data line bent portion;
at least one data electrode in the pixel region, the data electrode having a first data electrode section and a second data electrode section, the first data electrode section and the second data electrode section intersecting at a data electrode bent portion;
at least one common electrode in the pixel region, the common electrode having a first common electrode section and a second common electrode section, the first common electrode section and the second common electrode section intersecting at a common electrode bent portion; and
at least one common line in the pixel region, the common line crossing the data lines, the data electrode, and the common electrode, wherein the common line is formed on a different layer in a cross sectional view taken perpendicular to the surface of the substrate from the gate line.
2. (Original) The liquid crystal display device of claim 1, wherein the common line crosses the data line at the data line bent portion, the data electrode at the data electrode bent portion, and the common electrode at the common electrode bent portion.
3. (Original) The liquid crystal display device of claim 1, wherein the common line is substantially parallel to the gate line.
4. (Original) The liquid crystal display device of claim 1, wherein the first data line section, the first data electrode section, and the first common electrode section are substantially parallel.

5. (Original) The liquid crystal display device of claim 1, wherein the common lines comprise a transparent conductive material.

6. (Original) The liquid crystal display device of claim 1, wherein the data electrodes comprise a transparent conductive material.

7. (Original) The liquid crystal display device of claim 1, wherein the common electrodes comprise a transparent conductive material.

8. (Original) The liquid crystal display device of claim 1, wherein the data electrodes partially overlap at least one of the gate lines.

9. (Original) The liquid crystal display device of claim 1, wherein a storage capacitor is formed where the common line crosses the data electrode.

10. (Currently Amended) A liquid crystal display device comprising:
a plurality of gate lines on a first surface of a substrate;
a plurality of data lines, crossing said gate lines, such that at least one pixel region is defined by the data and gate lines, at least one of said data lines defining the pixel region, the data lines having a plurality of data line segments, the data line segments intersecting at data line bent portions[[:]], wherein each pixel region includes:
at least one data electrode in the pixel region, [[the]] each data electrode having a plurality of data electrode segments, the data electrode segments intersecting at data electrode bent portions; and
at least one common electrode in the pixel region, [[the]] each common electrode having a plurality of common electrode segments, the common electrode segments intersecting at common electrode bent portions; and
at least one light shielding layer on the pixel region, the light shielding layer crossing the data lines, the data electrode, and the common electrode at respective ones of the data line bent portions, the data electrode bent portions and the common electrode bent portions, wherein the at

least one light shielding layer is formed on a different layer in a cross sectional view taken perpendicular to the surface of the substrate from the gate lines.

11. (Currently Amended) The device of claim 10, wherein one of the at least one light shielding layers is a common line.

12. (Currently Amended) An in-plane switching mode liquid crystal display device comprising:

gate lines formed on a substrate;

data lines having a plurality of data line segments intersecting at a plurality of bent portions to cross the gate lines, crossings of the data and gate lines defining a pixel region [[;]], wherein each pixel region includes:

a plurality of data electrodes and common electrodes each having a plurality of segments intersecting at bent portions;

a common line on [[the]] bent portions of the data lines, the data electrodes and the common electrodes; and

a plurality of auxiliary common lines on [[the]] bent portions of the data electrodes and the common electrodes.

13. (Currently Amended) The device of claim 12, wherein the common lines are ~~formed in~~ substantially parallel with the gate lines.

14. (Original) The device of claim 12, wherein the common lines include transparent conductive film.

15. (Original) The device of claim 12, wherein the data electrodes include transparent conductive film.

16. (Original) The device of claim 12, wherein the common electrodes include transparent conductive film.

17. (Original) The device of claim 12, wherein at least one of the common lines is formed within the pixel region.

18. (Original) The device of claim 12, wherein the data lines, the data electrodes, and the common electrodes have at least one bent portion.

19. (Currently Amended) The device of claim 12, wherein the common lines are formed integrally with the common electrodes and the common lines and common electrode are formed on the data electrode.

20. (Original) The device of claim 12, wherein the data electrodes are connected with the gate lines.

21. (Original) The device of claim 12, wherein the data electrodes overlap the gate lines.

22. (Original) The device of claim 12, wherein the data and common electrodes are on different layers.

23. (Original) The device of claim 12, further comprising a light shielding layer on the bent portion between the data electrodes and the common electrodes.

24. (Currently Amended) An in-plane switching mode liquid crystal display device, comprising:

gate lines on a substrate;

data lines having a plurality of data line segments intersecting at data line bent portions to cross the gate lines, the data and gate lines defining a pixel region [[:]] wherein each pixel region includes:

a plurality of data electrodes having a plurality of data electrode segments intersecting at data electrode bent portions;

common electrodes having a plurality of common electrode segments intersecting at a plurality of common line bent portions, the common electrodes being connected with each other at the bent portions; and

common lines on the bent portions of the data lines, the data electrodes and the common electrodes.

25. (Currently Amended) The device of claim 24, wherein the data electrodes are ~~connected~~ overlapped with the gate lines.

26. (Currently Amended) The device of claim 24, wherein the common electrodes are connected together by ~~with~~ the common lines.

27. (Original) The device of claim 24, wherein the data electrodes include a first electrode and a second electrode.

28. (Original) The device of claim 27, wherein the first electrode has a plurality of bent portions.

29. (Original) The device of claim 27, wherein the first electrode is connected with the second electrode.

30. (Original) The device of claim 24, further comprising a light shielding layer on the bent portion between the data electrodes and the common electrodes.